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Highly efficient power amplifiers (PAs) and associated linearization techniques have been developed to accommodate the explosive growth in the data transmission rate and application of massive multiple input multiple output (mMIMO) systems.

A critical issue with massive MIMO is the costly circuit power consumption, which is proportional to the number of antennas. This paper develops low-complexity power allocation techniques to apply beamforming and to maximize the energy efficiency of massive MIMO while meeting users' quality-of-service requirements.

In the second part, several schemes of compensation and/or reduction of imperfections are explored, including linearization of power amplifiers, compensation of the characteristics of analog-to-digital converters and CFO compensation for OFDM modulation. The third and last part demonstrates the

use of some of these techniques in modern wireless-communication systems, such as full-duplex transmission, massive MIMO schemes and Internet of Things applications.

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