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9UZX8Y - SHANNON LARSON

"Anti-Synchronization Control for Fractional-Order Nonlinear Systems Using Disturbance Observer and Neural Networks." Robust Adaptive Control for Fractional-Order Systems with Disturbance and Saturation. Ed. Mou Chen, Shuyo Shao, and Peng Shi. ASME-Wiley, 2018.

Control and Synchronization of Fractional-Order Chaotic ...

Fractional-order systems provide extra degrees of freedom and control capability with integer-order differential equations as special cases. Synchronization is a necessary function in any communication system and is rather hard to be achieved for chaotic signals that are ideally aperiodic.

This paper addresses the synchronization issue for the drive-response fractional-order memristor-based neural networks (FOMNNS) via state feedback control. To achieve the synchronization for considered drive-response FOMNNS, two feedback controllers are introduced. Then, by adopting nonsmooth analysis, fractional Lyapunov's direct method, Young inequality, and fractional-order differential ...

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Routh-Hurwitz conditions and using specific choice of linear controllers, it is shown that the fractional-order autonomous system can be controlled to its equilibrium points. In addition, the synchronization of the fractional-order system and the fractional-order Liu system is studied using active control technique.

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Chaos and control of a three-dimensional fractional order ...

Control and Synchronization Of A Class Of Uncertain ...

Next, a synchronization control scheme for two different fractional order discrete-time systems with hidden attractors is reported, where the master system is a two-dimensional system that has been reported in the literature. Numerical results are presented to confirm the results.

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order Chen system when the order is less than 3. A Linearizing feedback technique has successfully been applied on this chaotic system. In (Li et. al, 2003), synchronization of fractional order chaotic systems has been studied.

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Control and Synchronization of Fractional{Order Differential Equations of Phase{Locked Loop Vernet{Thanh Pham 1, Mattia Frasca , Riccardo Caponetto1, Thang Manh Hoang2, and Luigi Fortuna1 1 Dipartimento di Ingegneria Elettrica Elettronica e Informatica, Universit  degli Studi di Catania, 95125 Catania, Italy (E-mail: mfrasca@diees.unict.it)

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